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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CIP of 10/611,172 07/02/2003

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-364099 12/16/2002

JAPAN 2003-353924 10/14/2003

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 22	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>				

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## TITLE

Semiconductor integrated circuit with test circuit

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